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Design Lab (A)) Page 1 of 3 LAB

**A-03 (Week 4) Analysis of
Combinational Logic Circuits
Objectives 1. To obtain Boolean
expression for the output based on
the given logic diagram 2. To
simplify the logic circuit using**

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**Boolean algebra techniques,
construct the simplified circuit and
verify the truth table for the
simplified expression.**

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**block or pass the gate as an output
of the result.**

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**A1: Since digital logic designers
build electronic components which
use both electrical and**

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computational characteristics, the design is foundational to the fields of electrical and computer engineering.

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Computer Logical Organization

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refers to the level of abstraction above the digital logic level, but below the operating system level. At this level, the major components are functional units or subsystems that correspond to specific pieces of hardware built from the lower level building blocks.

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In digital logic, the inputs and output of a function are in the form of binary numbers (boolean values) i.e., the values are either zero (0) or one (1). Therefore, digital logic is

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also known as 'Boolean logic'.

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is a basic one for both the electrical and computer engineering field students. DLD is utilized to develop hardware, like circuit boards, and microchip processors. And these hardware components will be used in various electronic devices. So, having knowledge about Digital

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